BRANCH PREDICTION METHOD USING ADDRESS TRACE

Abstract of the Disclosure

A branch prediction method using an address trace is described. An address trace corresponding to an executed instruction is stored itself with a decoded form. After appointing a start address and an end address of a repeated routine, current routine accessing times and total accessing times are compared with each other, confirming the end of the routine and storing address information of the next routine. Therefore, access information of the repeated routine can be stored using a small amount of a trace cache.

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